

<b>Notice of References Cited</b>		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		10/799,248	GRAY ET AL.	
Examiner		Art Unit		Page 1 of 1
Mahmoud Dahimene		1765		

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-4,136,615	01-1979	Pozniak, Norman J.	101/401.1
*	B	US-2004/0077178	04-2004	Yang et al.	438/710
*	C	US-2001/0021419	09-2001	Luthje et al.	427/421
*	D	US-5,688,366	11-1997	Ichinose et al.	438/754
*	E	US-2003/0185971	10-2003	Saksa et al.	427/96
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages
	U	S. Wolf and R.N. Tauber, (Silicon Processing for the VLSI Era, Volume 1- Process Technology, Lattice Press, 1986)(pages 522, 524, 562)
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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